Appl. No. 10/693,285

Amdt. dated March 17, 2006

Reply to Notice of Allowance of December 20, 2005

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## N THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Honda Yang

Assignee: Adaptec, Inc.

PROGRAMMABLE LOGIC DEVICE PARTITIONING METHOD

Title: FOR APPLICATION SPECIFIC INTEGRATED CIRCUIT

PROTOTYPING

Serial No.: 10/693,285 Filed: October 23, 2003

Examiner: Yelena Rossoshek Group Art Unit: 2825

Docket No.: ADPT1070 Confirmation No. 2347

Monterey, CA March 17, 2006

Mail Stop Issue Fee Honorable Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## AMENDMENT UNDER RULE 312

Dear Sir:

In response to the Notice of Allowance dated December 20, 2005, please amend the above-identified application as follows:

- 1. Amendments to the Specification begin on page 2 of this paper;
- 2. Amendments to the Drawings begin on page 11 of this paper and include an attached replacement sheet; and
  - 3. Remarks begin on page 12 of this paper.

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